Zachary A. Levy

Contact: ZacharyL@knights.ucf.edu

Location: Melbourne, FL

Objective: To gain more Electrical Engineering experience in Digital Control Systems Design

Education

***University of Central Florida**, Orlando, FL*

Major: Electrical Engineering   Graduated: Spring 2014   GPA: 3.8/4.0

Relevant Coursework

Senior Design 1 & 2	Autonomous Robotic Systems	Communication Systems
HDL Applications (Verilog)	Digital Signal Processing	Embedded Systems
Biomedical Nanotechnology	Linear Control Systems	Electromagnetic Fields
Semiconductor Devices	Intro to C Programming	Dynamics
Networks & Systems	Digital Systems (FPGA Verilog coding)	Analog and Digital Comm.
Microelectronics 1&2	Computer Organization	Probability & Statistics
Engineering Analysis	Modern Physics	Statics

Computer Skills

MATLAB	MS Office (Word, Excel, PowerPoint,Visio)
IBM DOORS	PLM (Project Data Management Database)
Multisim (Circuit Simulation)	Xilinx ISE (FPGA Verilog coding environment)
Eagle (PCB CAD)	Code Composer Studio (Embedded Processor Coding Environment)

Accomplishments

Eta Kappa Nu Honor Society	UCF President's & Dean's List	Silver Pegasus Scholarship
Phi Theta Kappa Honor Society	BCC Mathematics & Physics Awards	Medallion Scholar Scholarship

Clubs/Organizations

Baptist Collegiate Ministries   University House of Prayer

Project/Equipment Experience

Designed and Developed Printed Circuit Board for Senior Design	Analyzed AM/FM Communication Signals
Worked with Senior Design Group for two semester design project	Performed Circuit Troubleshooting
Formatted Senior Design Final Documentation	Utilized Electronics Lab Equipment

Work Experience

Harris Corporation, Melbourne, FL – *Full Time Employee: Electrical Engineer 1*

Digital Design Team   May 2014 – Present

- FPGA and Circuit Card Assembly Design Training
- Exposed to Team Oriented Engineering Projects

University of Central Florida, Orlando, FL – *Lockheed Martin Work Experience Participant*

Hellfire Missile Systems Engineering Team   Jan-Aug 2013

- Obtained DoD Secret Security Clearance, March 2013
- Participated in Laboratory Qualification Tests
- Wrote Test Procedures for Analysis Equipment
- Organized Document Traceability using DOORS
- Collaborated with Engineering team to complete project goals
- Drew Diagrams for Test Procedures and Equipment
- Updated Risk Management database